Phases of Electronic System Design
DESIGN LEVELS

THE ARCHITECTURE LEVEL

High-Level Functional Components: Block Diagram

System Input/Output Behavior: Hardware Design Language

THE LOGIC LEVEL (Technology Independent)

Register Transfer Level

Gate Level

Switch Level

THE PHYSICAL LEVEL

Organization of Physical Components in a Technology

ICs: SSI, MSI, LSI, VLSI

DTL, TTL, LS, ECL, CMOS, PLA, PAL, FPGA, ASIC