ISVLSI 2020 will host a Student Research Forum (SRF), providing opportunity for recently graduated or final-phase BS/MS/Ph.D. students to present their research/thesis work to a broad audience in the VLSI community from both industry and academia. The Student Research Forum will help students to establish contacts for entering the job market. Additionally, representatives from industry and academia will get a glance of cutting-edge research performed by the future leaders of the VLSI community.

Eligibility:
- Students who are currently enrolled in a PhD program, or have finished their BS/MS dissertation/thesis within the last 12 months,
- The research topics should be of relevance to ISVLSI community,
- Students who have presented in prior ISVLSI Student Research Forum must clearly indicate additional (30%) new technical content compared to the previous submission.

Submission Guidelines:
Please submit a paper in IEEE conference format describing the problem addressed in the thesis, novelty of the solution, observations and analysis. The focus should be on the scientific challenges, the originality and the quality of the solution.
- The papers have a 6 page limit.
- The submission can include author information (student and advisor).
- A single PDF file should be submitted via https://edas.info/27536 under the track of Student Research Forum (SRF).

Benefits to the Selected Researchers:
- 6-page publication in the ISVLSI 2021 proceedings (indexed in IEEE eXplore).
- Presentation in the Student Research Forum poster session.
- Travel support for selected researchers.
- Networking with attendees from industry and academia.
- Opportunity to publicize work and distribute flyers summarizing their research work.

For Additional Information, please contact
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Important Dates
- Submission Deadline: February 21, 2021
- Acceptance Notification: April 4, 2021